

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings of claims in the application:

Listing of Claims:

1 - 13 Canceled.

1 14. (Currently amended) A method of generating a noise function output at a
2 first location comprising:
3 generating M random number outputs, each random number output representing a
4 pulse, using a single first stage and M parallel second stage circuitries;
5 determining ~~the~~ a measure of relevance of each of the M random number outputs
6 to identify one or more relevant random number outputs from among the M random number
7 outputs~~said noise function output~~; and
8 accumulating each of an effective weighting computed for each relevant random
9 number output.

1 15. (Original) A method according to claim 14 wherein generating includes:
2 storing said first stage inputs in a first register;
3 selecting a first set of bits from said first register;
4 storing said first set of bits in a second register; and
5 selecting a second set of bits from said second register, said second set of bits
6 representing said result of said single first stage circuitry.

1 16. (Original) A method according to claim 15 wherein generating, for each
2 parallel second stage module, further includes:
3 storing said result of said single stage circuitry in a second stage register; and
4 selecting a second set of bits from said second stage register, said second set of
5 bits representing one of said M random number outputs.

1 17. (Original) A method according to claim 16 wherein said each random
2 number output can be one of a phase 1 output and a phase 2 output.

1 18. (Original) A method according to claim 17 wherein said phase 2 output is
2 a full and exact representation of the desired random number output.

1 19. (Original) A method according to claim 18 wherein said phase 1 output is
2 an approximation of said phase 2 output.

1 20. (Original) A method according to claim 15 wherein selecting said first set
2 of bits includes:

3 utilizing a group of XORs, each XOR having its inputs pre-wired to various
4 locations of said first register, to select among the bits of said inputs.

1 21. (Original) A method according to claim 15 wherein selecting said second
2 set of bits includes:

3 utilizing a group of XORs, each XOR having its inputs pre-wired to various
4 locations of said second register, to select among the bits stored therein.

1 22. (Original) A method according to claim 16 wherein said selecting in each
2 parallel second stage module includes:

3 utilizing a group of XORs, each XOR having its inputs wired to a number of bits
4 representing the module number and to selected locations of said second stage register.

1 23. (Original) A method according to claim 19 wherein determining the
2 relevance includes:

3 testing phase 1 outputs to see if the pulses they represent fall approximately
4 within an area of interest about said first location; and

5 updating a queue with a positive indication for each pulse that falls within said
6 area.

1 24. (Original) A method according to claim 23 wherein said area of interest is
2 determined by a circle swept by a radius about said first location.

1 25. (Original) A method according to claim 24 wherein testing includes:
2 determining the distance between the location of a pulse as approximated by their
3 phase 1 output and said first location;
4 subtracting an allowable difference from said determined distance; and
5 comparing the result of said subtraction with said radius.

1 26. (Original) A method according to claim 25 wherein if said result is not
2 greater than said radius, then said pulse is determined to fall approximately within said area of
3 interest.

1 27. (Original) A method according to claim 19 wherein accumulating
2 includes:
3 fetching the full phase 2 output for each pulse determined to be relevant to said
4 noise function output;
5 determining what effect each said relevant pulse would have to said noise
6 function;
7 modifying by said effect the weight of said pulse given by said phase 2 output,
8 said modification yielding an effective weight; and
9 summing together effective weights for all pulses deemed to be relevant to said
10 noise function output, said sum representing the amplitude of the noise function at said first
11 location.

1 28. (Original) A method according to claim 27 wherein determining what
2 effect includes:
3 computing a sum of squared differences between said pulse as represented by its
4 phase 2 output and said location;

5 correlating said sum of squared differences with a corresponding effect factor;
6 and
7 obtaining said effect factor from a filter table.

1 29. (Original) A method according to claim 28 wherein modifying includes:
2 multiplying said effect factor by said weight.

30 - 37 (Canceled).

1 38. (Currently amended) An apparatus for generating a noise function output,
2 said noise function output being a combination of relevant pulses, the apparatus comprising:
3 a random number generator configured to generate M random number outputs in a
4 parallel fashion, each of said M random number outputs being an approximation of a full version
5 of each of said random number outputs;
6 a chooser device coupled to said random number generator, said chooser
7 determining if the pulse represented by each said random number output is relevant to the noise
8 function;
9 a queue coupled to said chooser device, said queue storing which of said M
10 random number outputs were determined relevant, said apparatus configured to generate the full
11 version of the relevant random number outputs;
12 an effect generator coupled to said queue, said effect generator modifying the
13 weight of each pulse as given by said full version according to the location of the pulse
14 represented by said full version;
15 and an accumulation device coupled to said effect generator to accumulate said
16 effect modified pulses weights, the result of said accumulation the amplitude of the noise
17 function.